

L Number	Hits	Search Text	DB	Time stamp
1	2	6357004.pn., 6507904.pn.	USPAT	2003/10/28 15:55
-	47	("6507904" "6633963" "6157721" "6292569" "6272533" "6327579" "6411943" "6289462" "5898835" "6253193" "6363488" "6389402" "6393569" "6427140" "6138239" "6148402" "6058482" "6173402" "5361359" "6006328" "6105137" "6192512" "5771354" "6014651" "6044467" "6192477" "5844986" "6215396" "6215396" "4627183" "5493279" "6038551" "6266413" "6289455" "6292830" "4423287" "6148387" "5953502" "6038667" "6061449" "6061449" "6311273" "6564326" "5222134" "5657388" "5802175" "5822739" "6005870" "6016307").pn.	USPAT USPAT	2003/10/22 14:04
-	2	6507904.PN., 6633963.PN.	USPAT	2003/10/21 17:51
-	3686	713/200,201,155-156,164-168,170-176,180,182,	USPATCCLS.	2003/10/21 18:29
-	0	380/30,259-266.CCLS	USPAT	2003/10/21 18:31
-	1324	380/30,259-266.CCLS.	USPAT	2003/10/21 18:31
-	274	340/545.1.CCLS.	USPAT	2003/10/21 18:32
-	2073	711/163,153,152,170,173.CCLS.	USPAT	2003/10/21 18:32
-	28	k5 and isolat\$4 same memory	USPAT	2003/10/21 18:45
-	3888	713/200,201,155-156,164-168,170-176,180,182,	USPATCCLS.	2003/10/21 18:46
-	65	and chipset or chip-set or "chip set" and (chipset or chip-set or "chip set")	USPATCCLS.	2003/10/21 18:47
-	0	(713/200,201,155-156,164-168,170-176,180,182 and (chipset or chip-set or "chip set"))	USPATCCLS.	2003/10/21 18:47
-	2	and (isolat\$ adj execut\$4) 713/200,201,155-156,164-168,170-176,180,182,	USPATCCLS.	2003/10/21 18:47
-	10	and (isolat\$ adj execut\$4) 380/30,259-266.CCLS. and (chipset or chip-set or "chip set")	USPAT	2003/10/21 18:49
-	55	711/163,153,152,170,173.CCLS. and (chipset or chip-set or "chip set")	USPAT	2003/10/21 18:52

-	3	(711/163,153,152,170,173.CCLS. and (chipset or chip-set or "chip set")) and (isolat\$5 with memory)	USPAT	2003/10/21 18:52
-	0	340/545.1.CCLS. and (chipset or chip-set or "chip set")	USPAT	2003/10/21 18:52
-	0	interface and decoder and "chipset storage" and "communication storage"	USPAT	2003/10/22 14:07
-	0	"processor nub loader hash"	USPAT	2003/10/22 14:08
-	0	(token adj bus) and "ICH interface bus"	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:09
-	36	ICH and chipset	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:21
-	35	(ICH and chipset) and interface and device	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:21
-	27	((ICH and chipset) and interface and device) and address and processor	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:21
-	150595	((memory or storage) with address)	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:22
-	9	((ICH and chipset) and interface and device) and address and processor) and ((memory or storage) with address)	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:22
-	0	((ICH and chipset) and interface and device) and address and processor) and (secur\$5 with information)	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:23
-	11	((ICH and chipset) and interface and device) and address and processor) and exchange\$4	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:26
-	10	((ICH and chipset) and interface and device) and address and processor) and key\$1	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:34
-	0	(ICH and chipset) and attestation	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:34
-	2	((ICH and chipset) and interface and device) and address and processor) and (execution adj mode)	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 14:35
-	12	((ICH and chipset) and interface and device) and address and processor) and decod\$5	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 15:00
-	710	public adj key and key adj certificat\$5	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 15:00
-	23	(public adj key and key adj certificat\$5) and chipset	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 15:10
-	1083	processor and device and bus and chipset	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 16:47
-	34	(processor and device and bus and chipset) and key and certificat\$4	USPAT; EPO; DERWENT; IBM_TDB	2003/10/22 17:31

-	122	nonce	USPAT	2003/10/28 11:37
-	2	hash adj log	USPAT	2003/10/28 11:39
-	0	nub adj loader adj hash	USPAT	2003/10/28 11:37
-	1818	chipset withstor\$5 with hash with log	USPAT	2003/10/28 11:40
-	0	chipset with stor\$5 with hash with log	USPAT	2003/10/28 11:40
-	0	chipset with store with hash with log	USPAT	2003/10/28 11:40
-	156	chipset with stor\$5	USPAT	2003/10/28 11:41
-	0	chipset with stor\$5 with hash	USPAT	2003/10/28 11:41
-	6	chipset with stor\$5 and hash	USPAT	2003/10/28 12:16
-	156	chipset with stor\$	USPAT	2003/10/28 12:16
-	13	chipset adj stor\$	USPAT	2003/10/28 12:21
-	1	6357004.pn. and chipset	USPAT	2003/10/28 12:23
-	34	chipset and (hash adj function)	USPAT	2003/10/28 12:26
-	19	chipset and hash and log	USPAT	2003/10/28 12:31
-	0	"log in chipset"	USPAT	2003/10/28 12:32
-	1	chipset with hash	USPAT	2003/10/28 12:37
-	42	"chipset comprising"	USPAT	2003/10/28 12:39
-	0	"chipset comprising" and hash and log	USPAT	2003/10/28 12:39
-	2	"chipset comprising" and hash	USPAT	2003/10/28 15:54